Stanford Research Systems, Inc.

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Volatility Statement for FS752

Nonvolatile Memory:

- 288kB of flash inside the microcontroller: not user modifiable, for storage and execution of instrument firmware
- 1MB of serial flash: for storage of instrument calibration and user settings. To clear, execute command "SYST:SEC:IMM".

Volatile Memory:

• 66kB SRAM inside the microcontroller: used for firmware execution. To clear, power cycle unit.

GPS Receiver:

• NEO-M8T GPS receiver from u-blox: used as a GPS receiver for timing. This chip has firmware not authored by SRS, and an unknown amount of nonvolatile and volatile memory internal to it required for its operation. The FS752 stores a very limited number of configuration settings here for proper operation with the FS752. Any non-volatile user settings used by the FS752 and stored within the GPS receiver may be cleared by executing the command "SYST:SEC:IMM". Please refer to u-blox for more detailed information if required.

Battery Backup:

• There is no battery backup of volatile memory within the FS752. All volatile memory is erased when the AC power switch is turned off.

Please feel free to contact SRS should you have any further questions.